

100

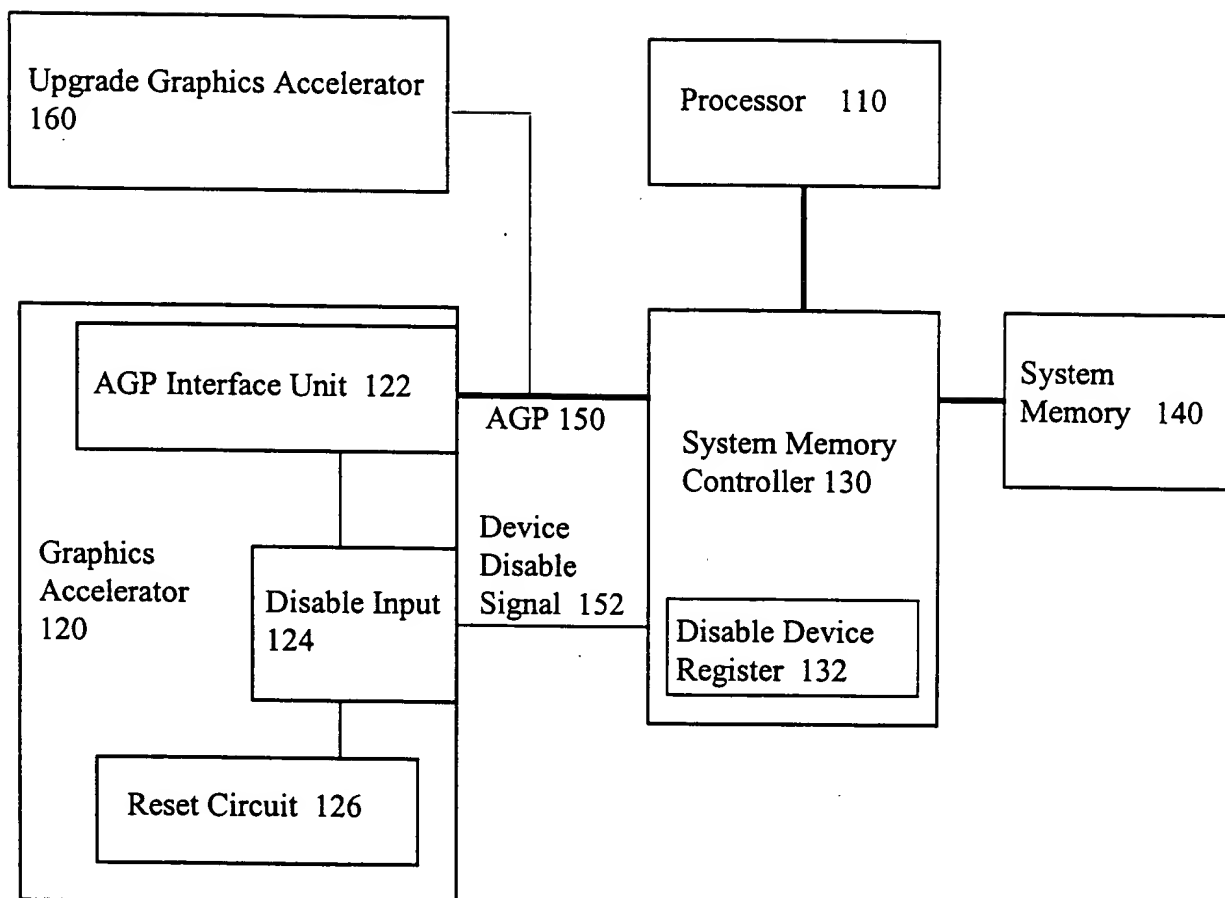


Figure 1

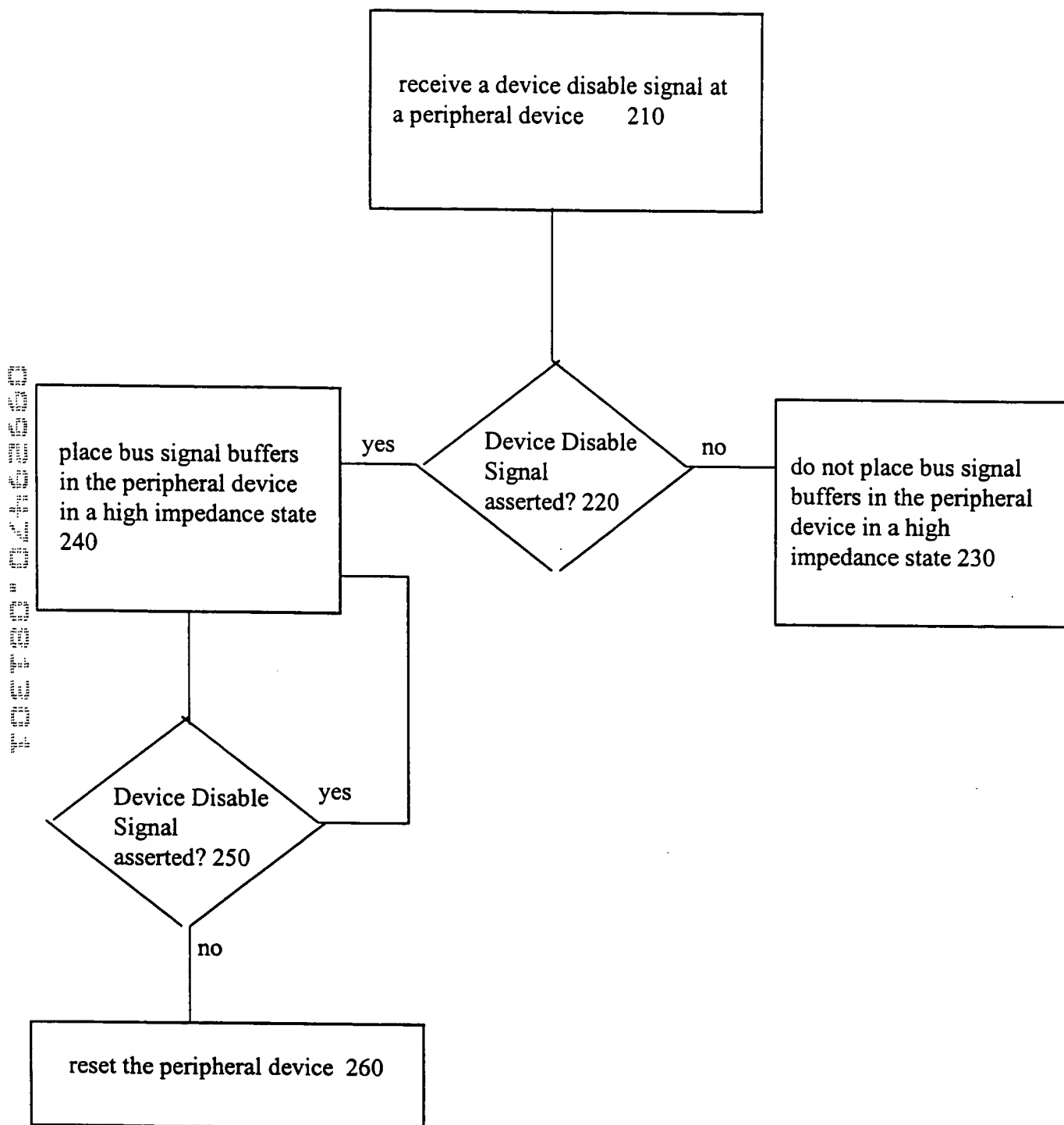


Figure 2

Page 1 of 1
Date: 11/11/2011
Time: 11:11:11 AM

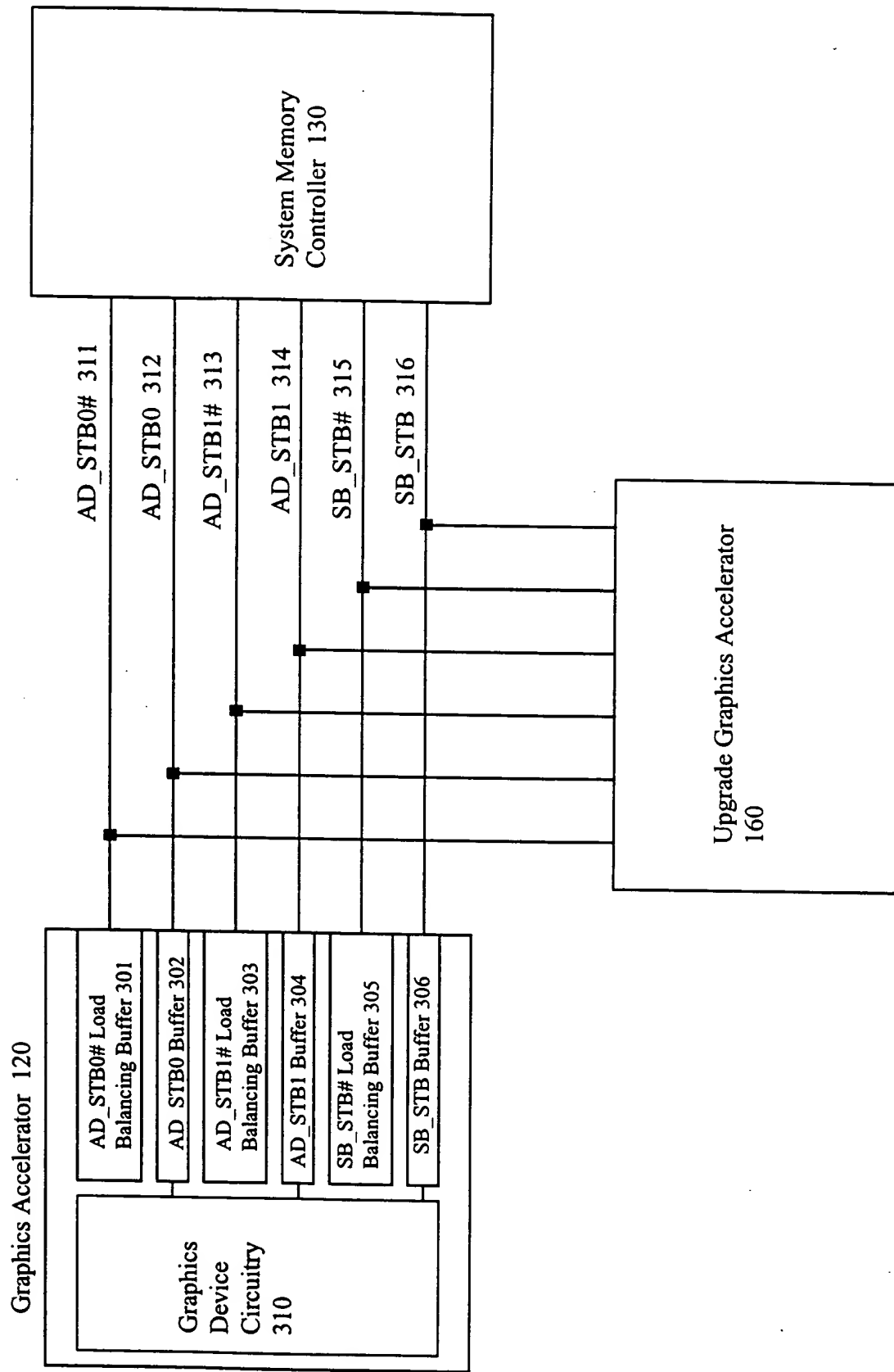


Figure 3

couple a graphics device to a graphics bus, the graphics device including a bus interface unit, the bus interface unit including a plurality of bus signal buffers to couple the graphics device to the graphics bus 410

provide a load balancing bus signal buffer to further couple the graphics device to the graphics bus, the load balancing bus signal buffer to provide load balancing on the graphics bus when an upgrade graphics device is installed 420

Figure 4